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Semiconductor Package And Method For Fabricating The Same Inventor(s): SHIN et al.

Application #: 09/574,541

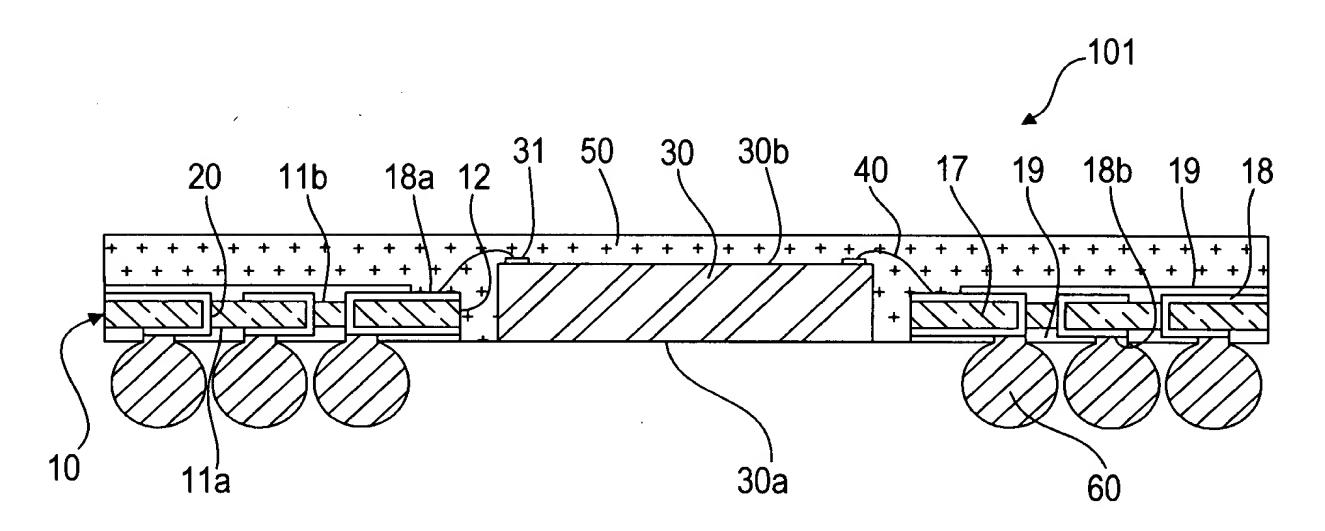


Fig.1

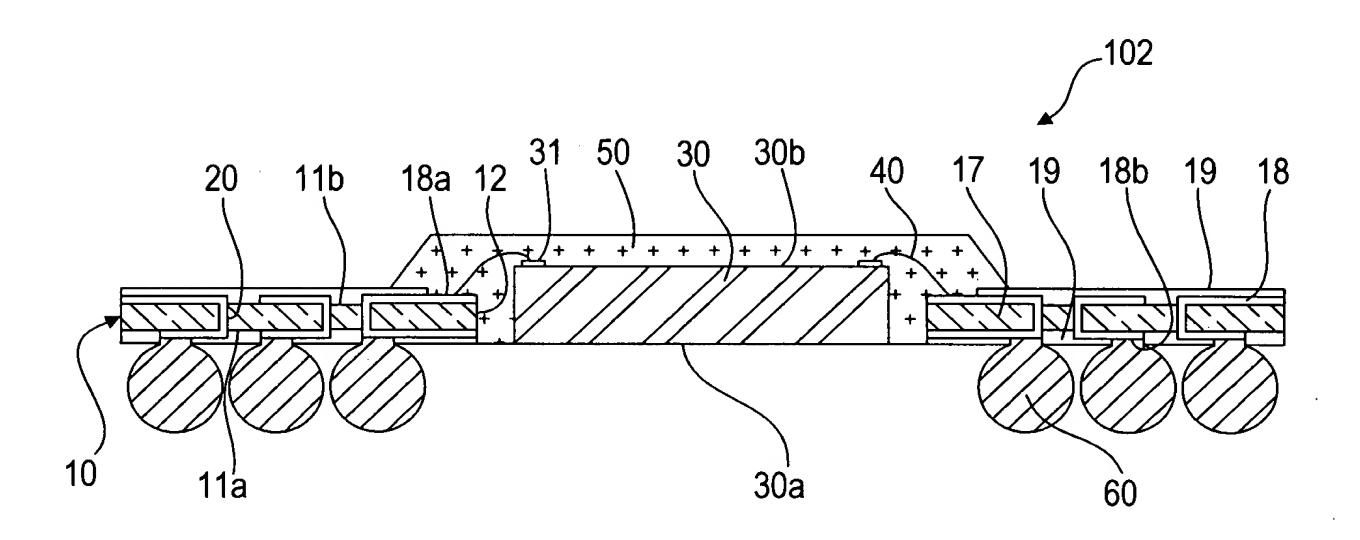


Fig. 2

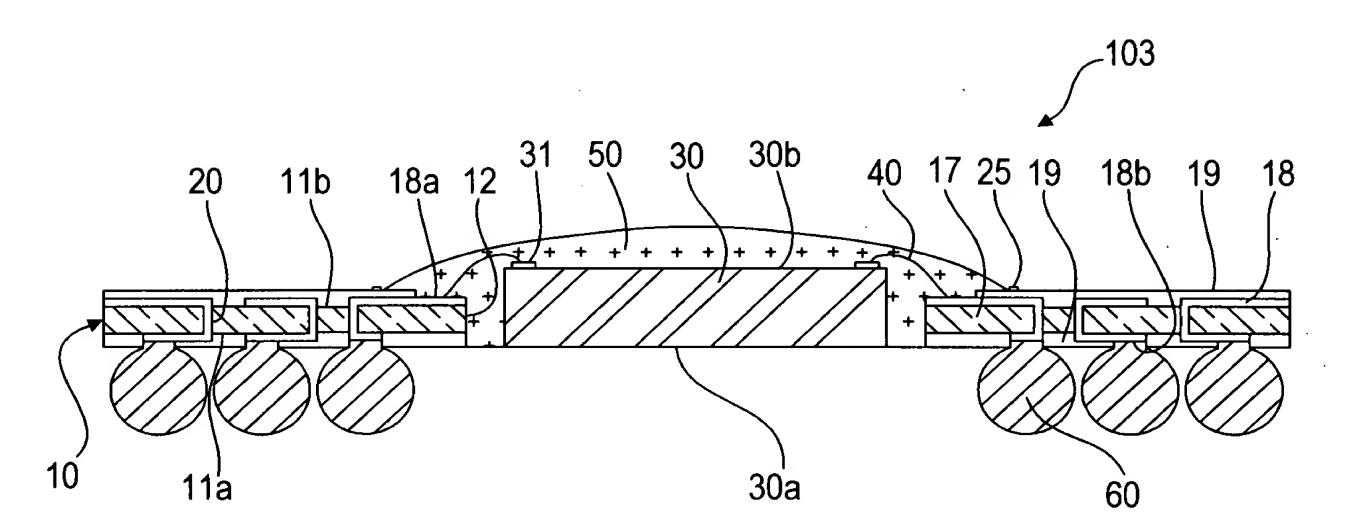


Fig.3

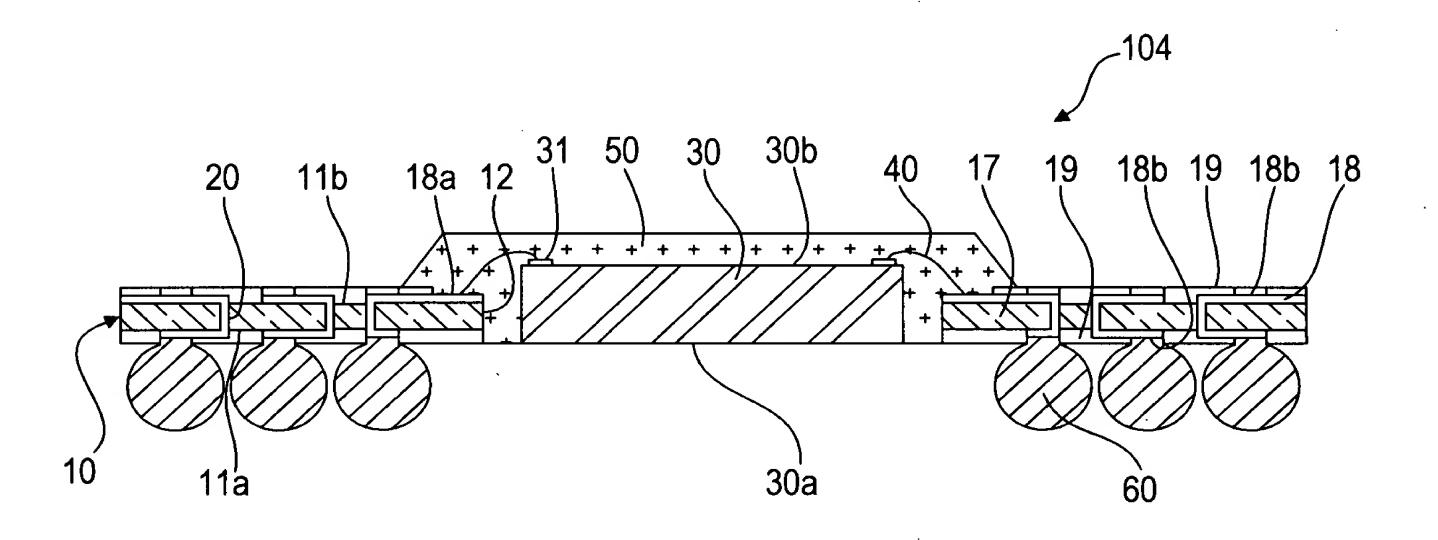


Fig. 4

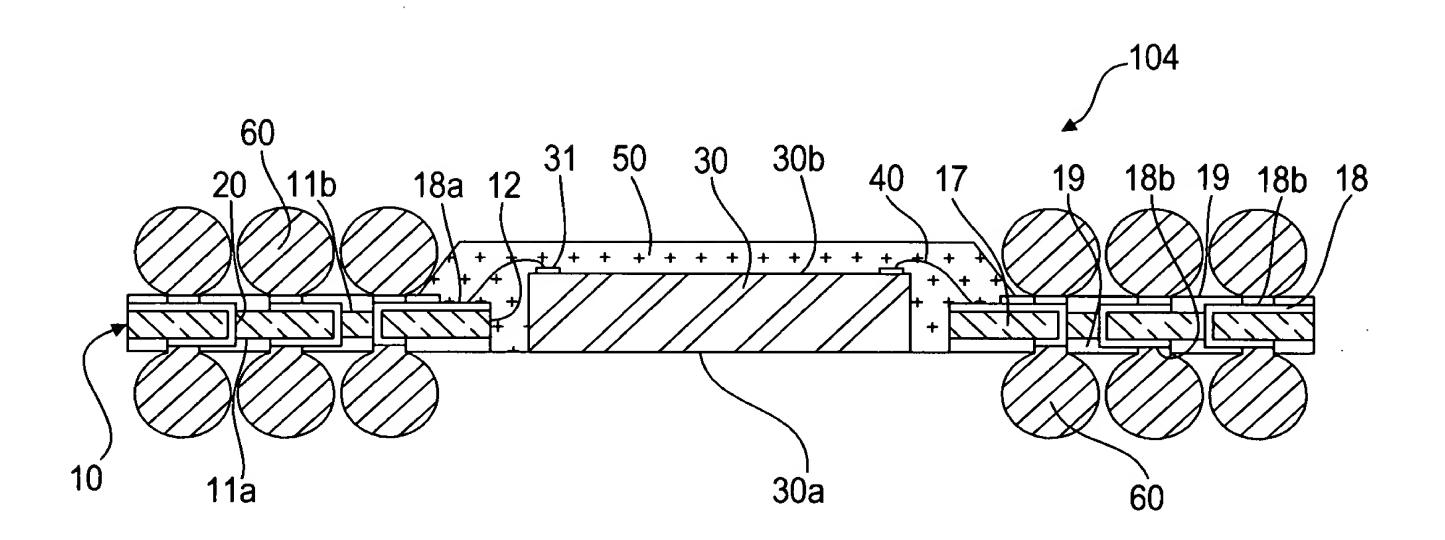
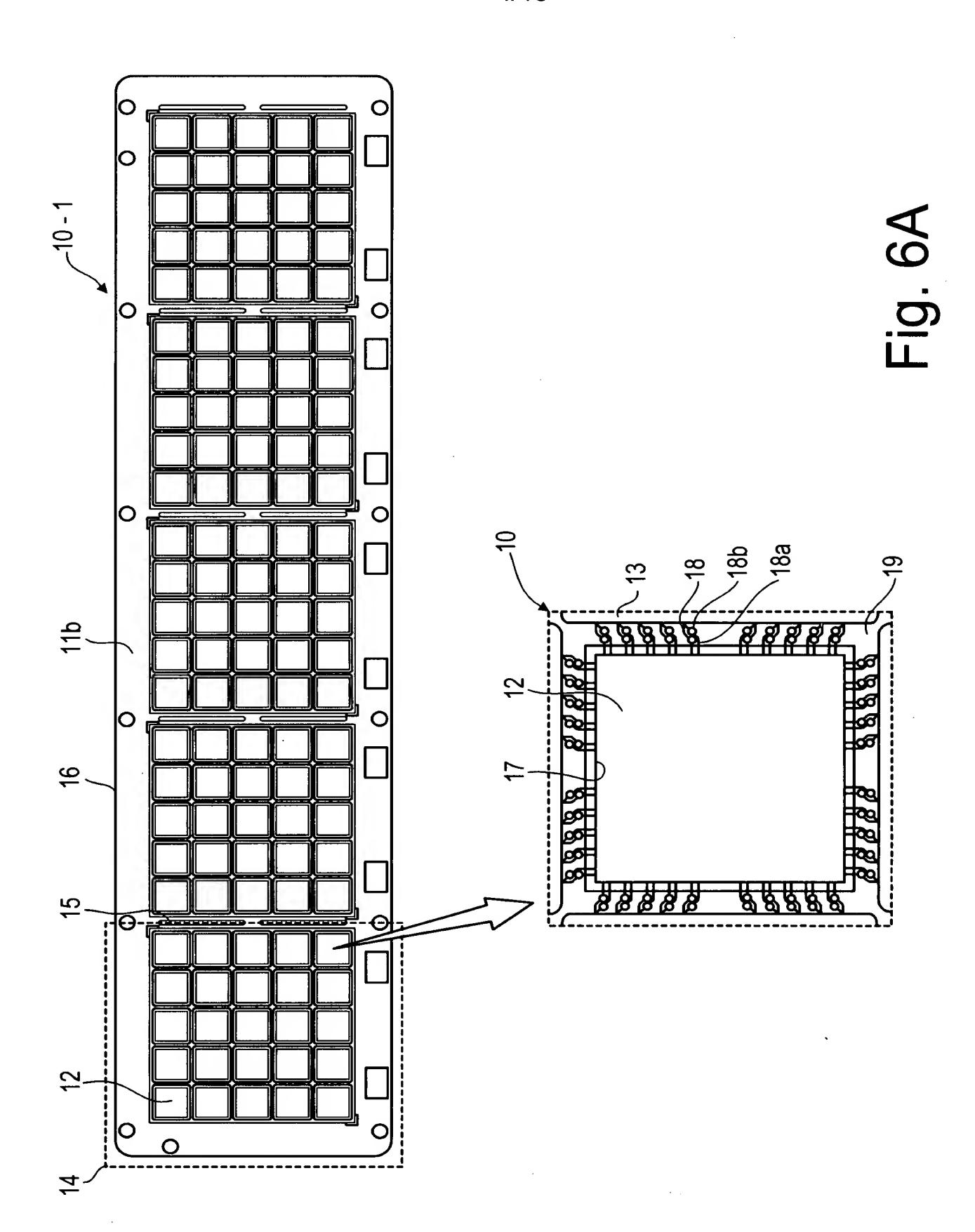
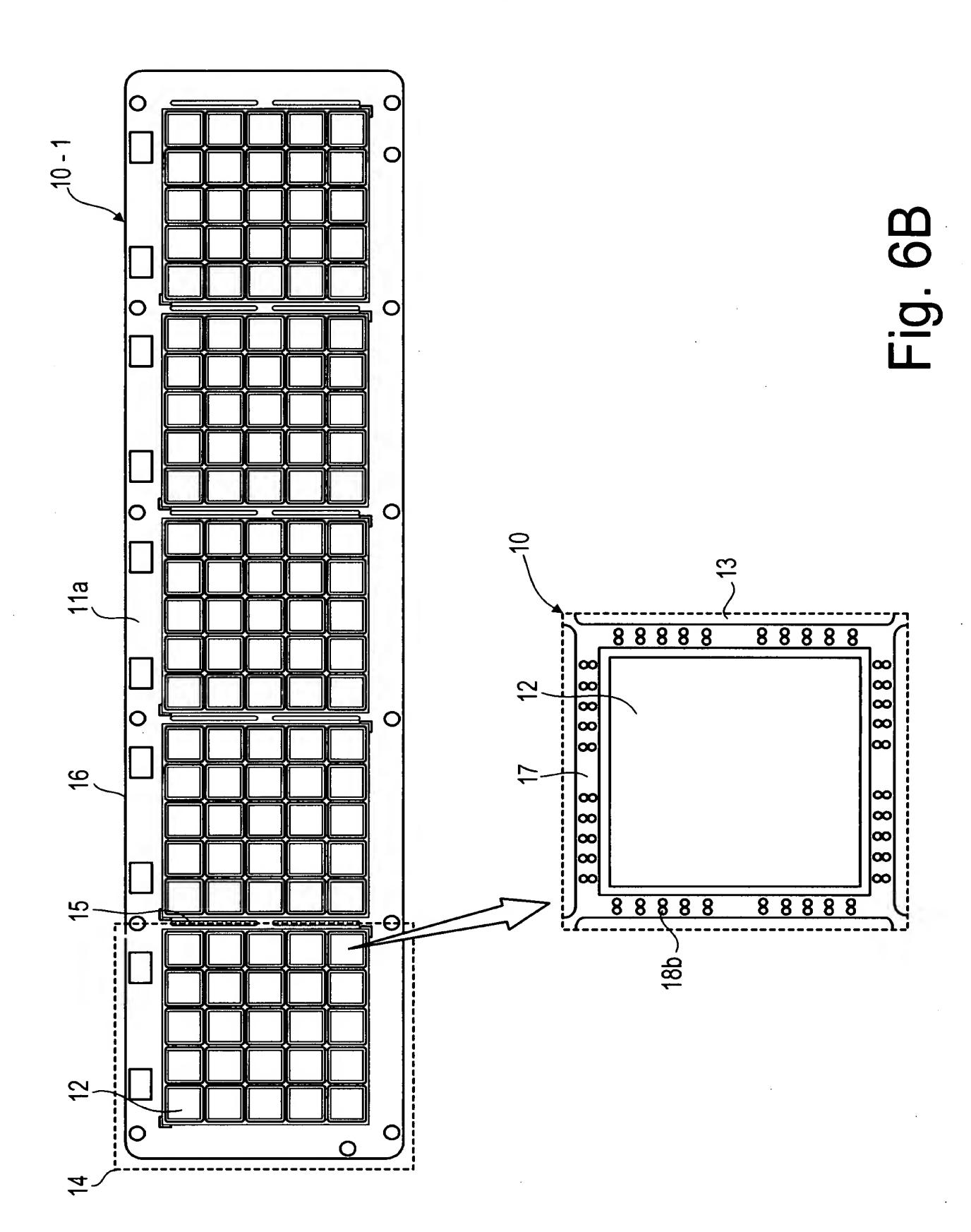


Fig. 5





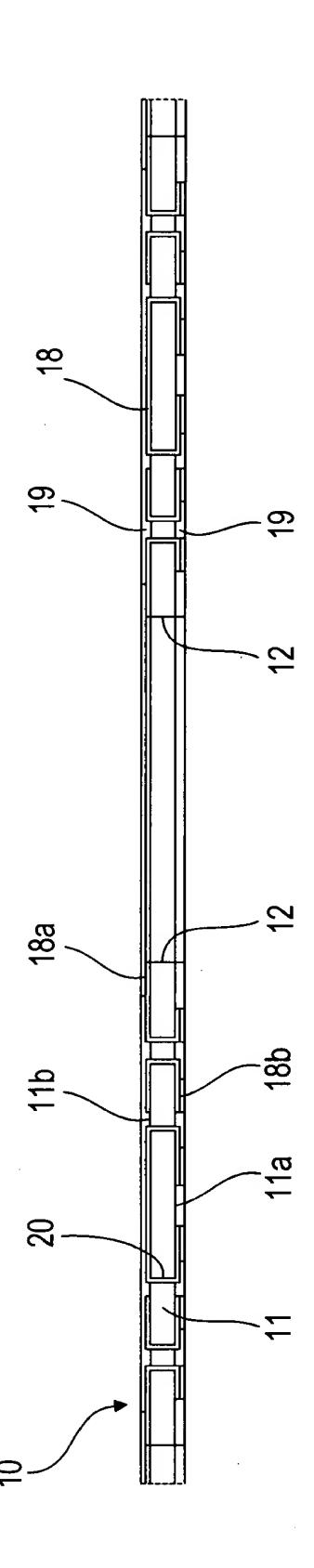
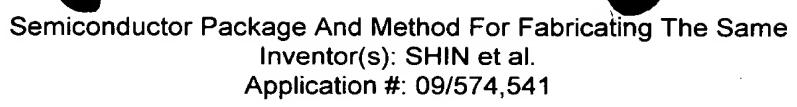
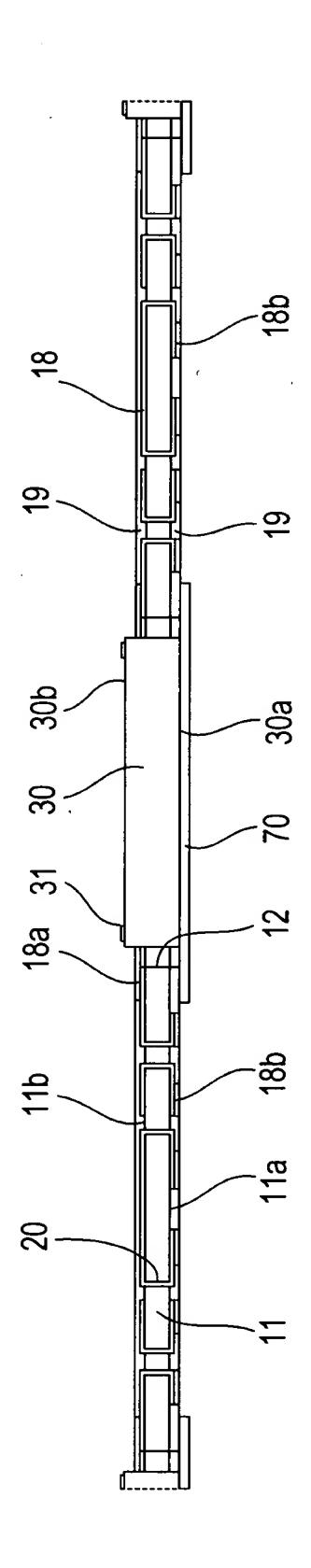


Fig. 7⊿



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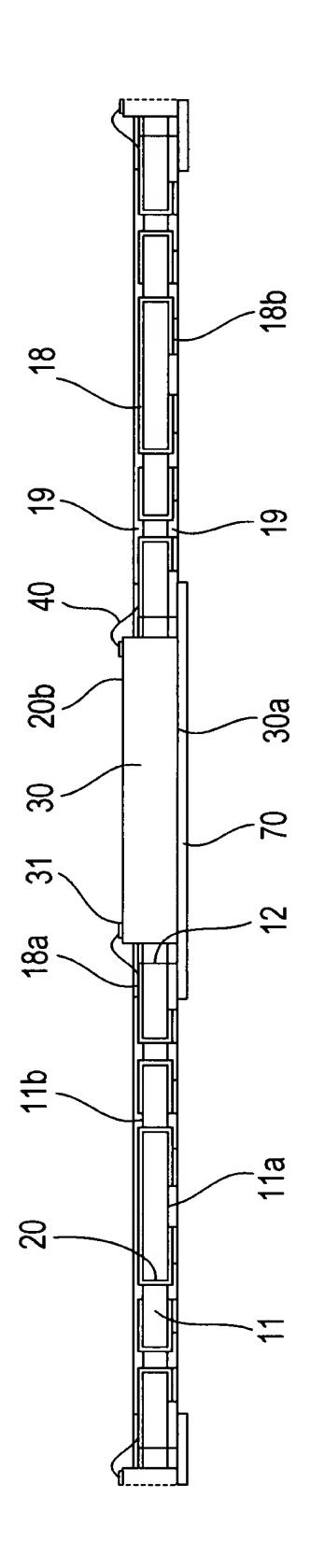
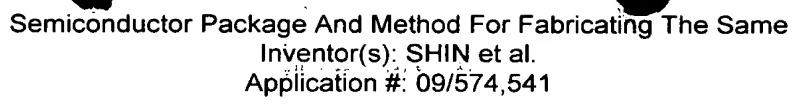
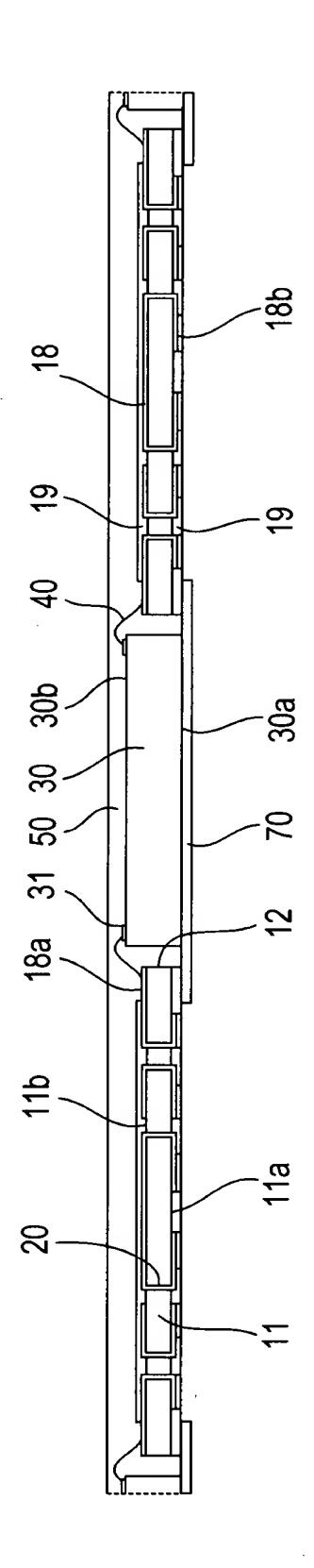
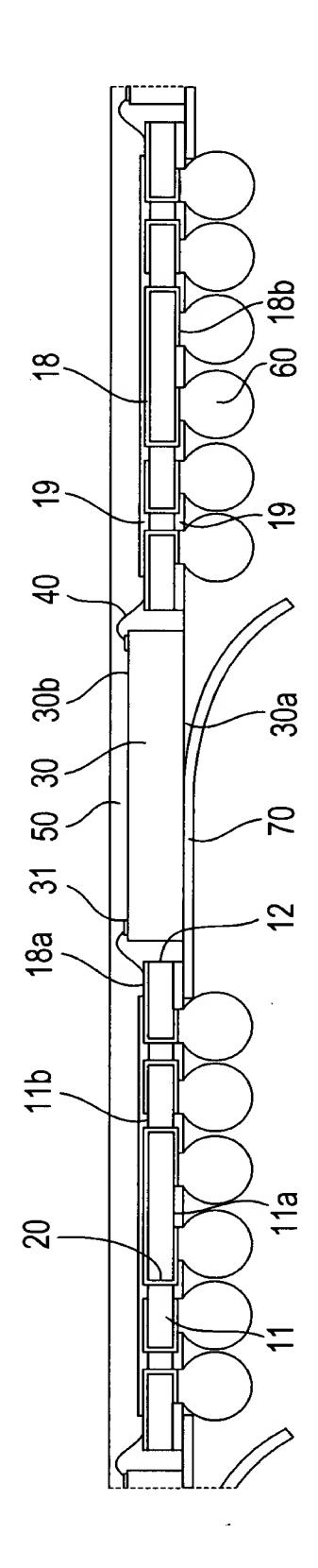


Fig. 7C







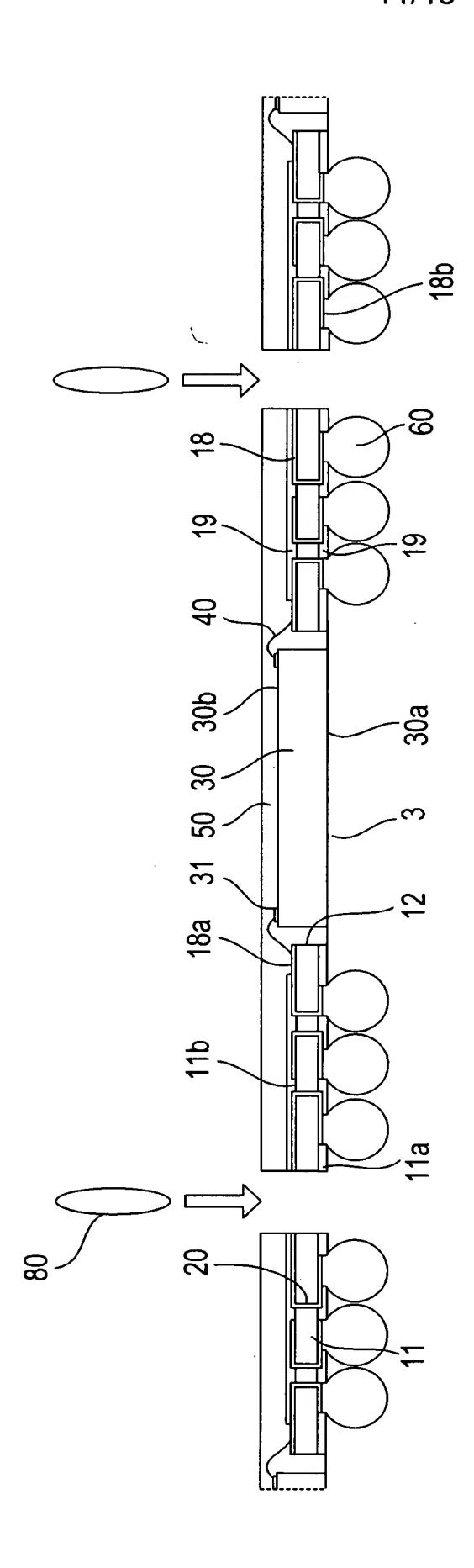


Fig. 7F

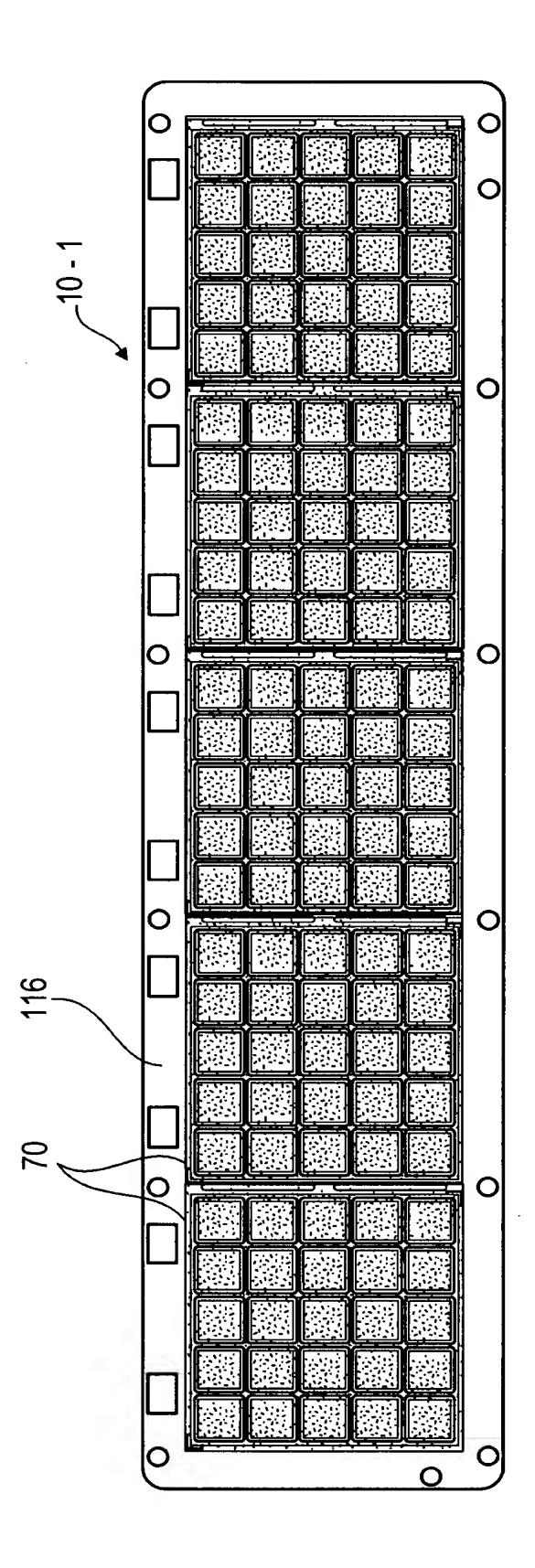


Fig. 8A

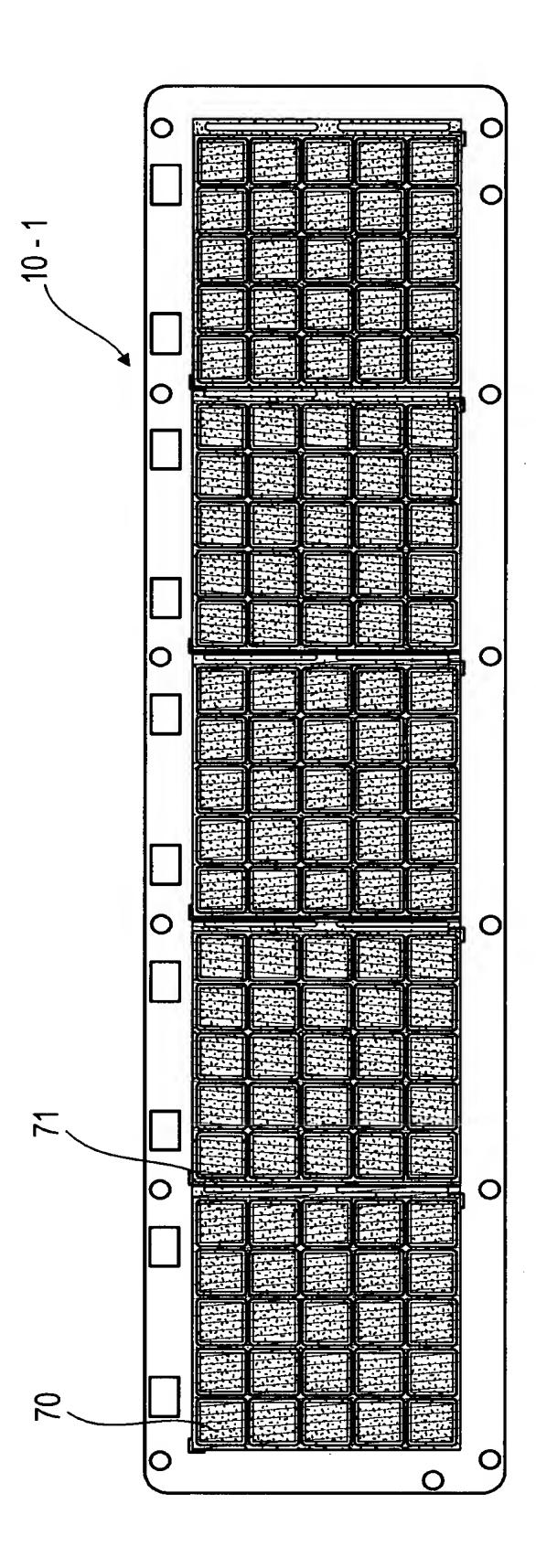


Fig. 8B

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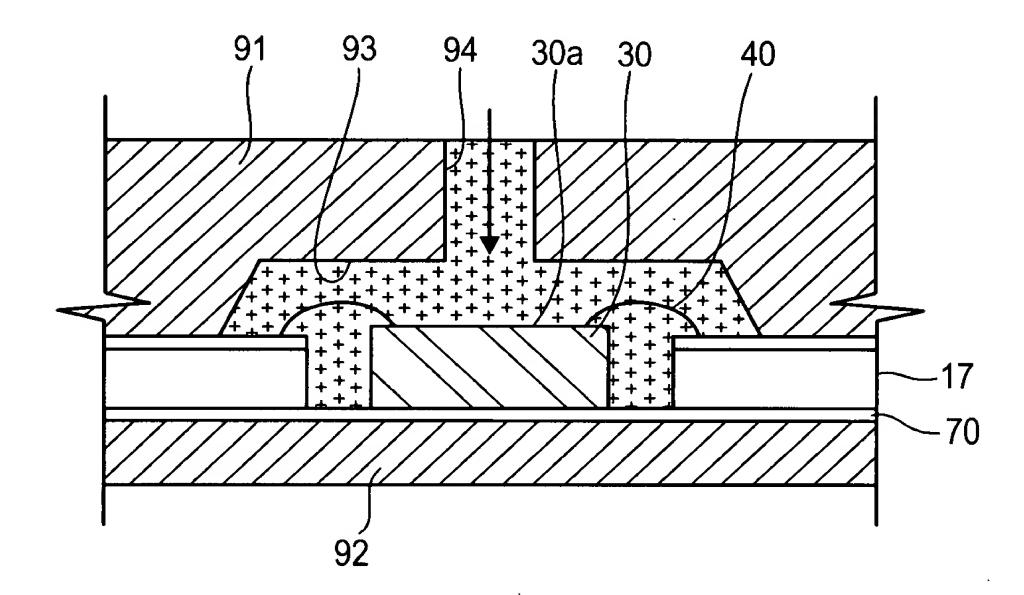


Fig. 9

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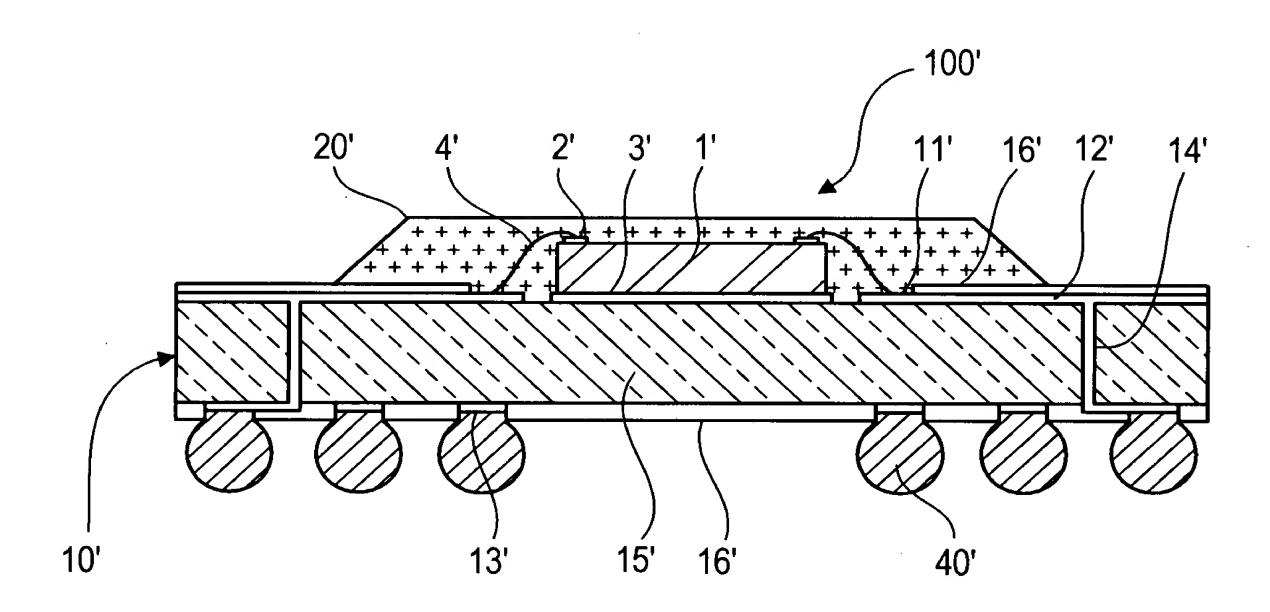


Fig. 10 (PRIOR ART)